

Design, Automation and Test in Europe (DATE) Conference 2015

DATE is a leading international event and unique networking opportunity for design and engineering of Systems-on-chip, Systems-on-Board and Embedded Systems Software. The conference is held at a different place each year, and this year it takes place in Grenoble, France. The duration of the event will be five days from March 9th till 13th. The DATE conference addresses all aspects of research into technologies for electronic and embedded system engineering. It covers the design process, test, and automation tools for electronics ranging from integrated circuits to distributed embedded systems. This includes both hardware and embedded software design issues. The conference scope also includes the elaboration of design requirements and new architectures for challenging application fields such as telecoms, wireless communications, multimedia, healthcare, smart energy and automotive systems.

One main purpose of this travel was to attend both the conference events in order to have up-to-date information regarding latest advancements in Embedded Systems Engineering and Systems-on-Chip, which also overlaps with the research interest of our department of Computer Engineering at Tallinn University of Technology. Meeting different well-known attendants from the industry and erudite professors who are expert in the field would be a great opportunity for everyone, specially researchers and PhD students to build an infrastructure for future collaborations or even planning for their future career. Also, The conference was complemented by an exhibition, which stated a unique networking opportunity for vendors of tools and services for hardware and embedded software for the design, development and test of Systems-on-Chip, IPs, Embedded Systems, ASICs, FPGAs and PCBs including a broad range of design reuse technologies and services.

The other main purpose of this travel was to attend the MEDIAN 2015 workshop which was part of the DATE conference, during which our accepted paper was presented by me during the poster session, titled “Extended Checkers for Control Part of Routers in Network-on-Chips”. Indeed, the different sessions during the conference and the workshop provided an open forum for presentations and can challenge the participants to think and discuss hot research topics in related fields.

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